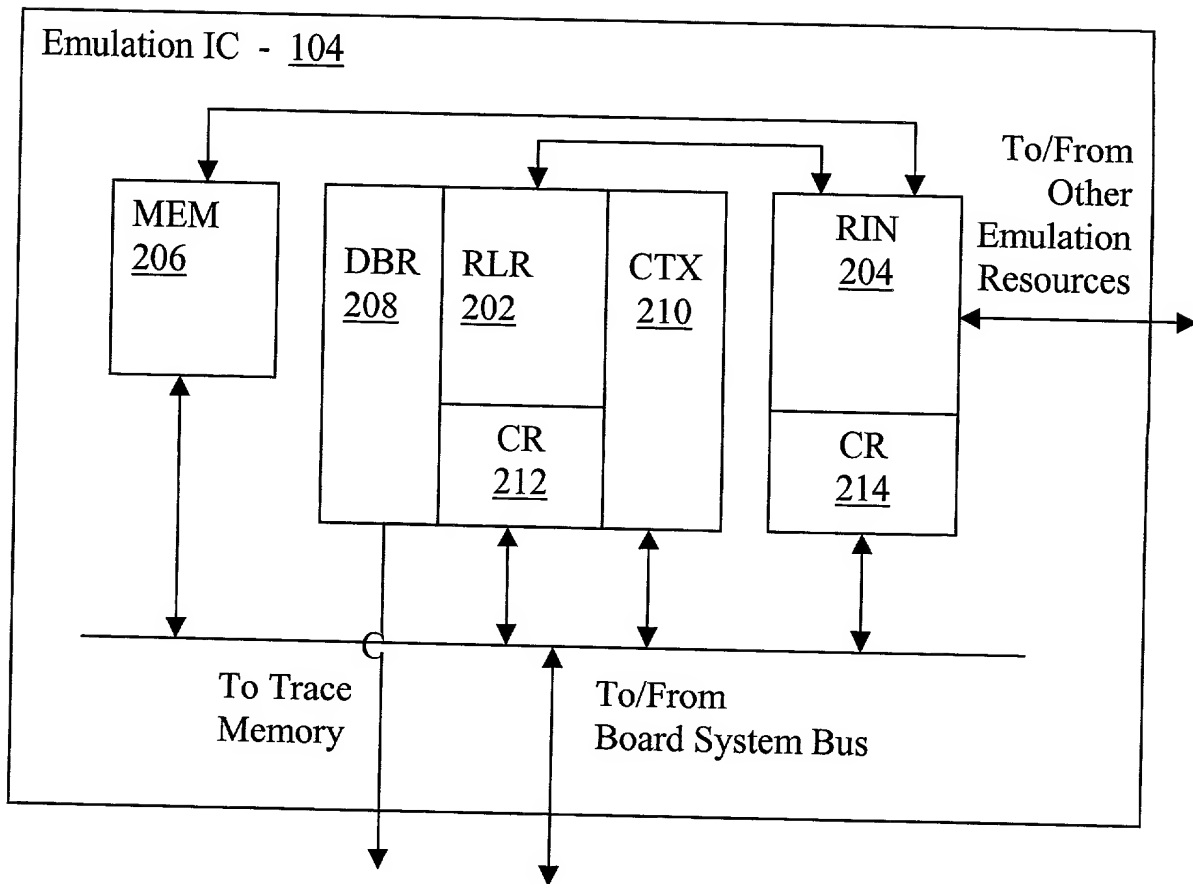
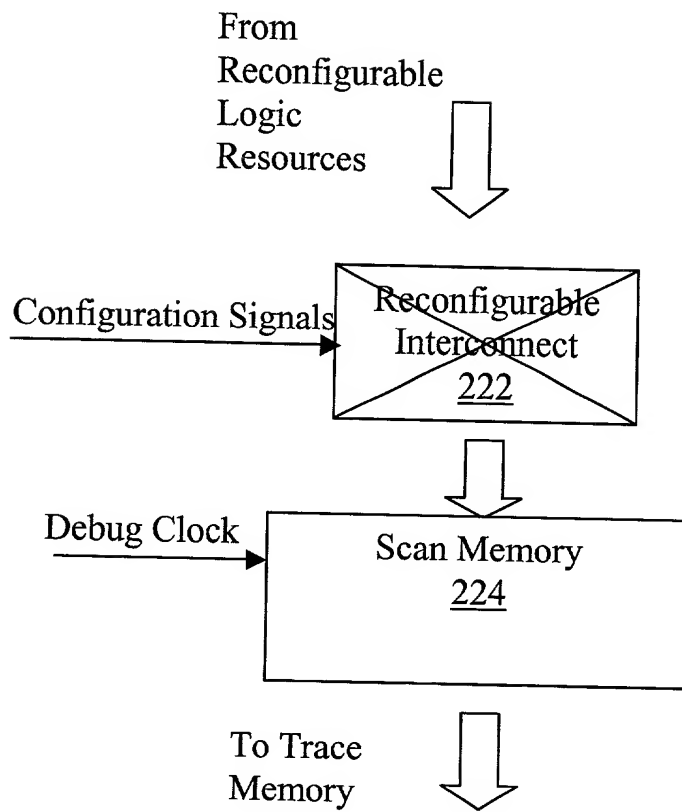


**Figure 1**



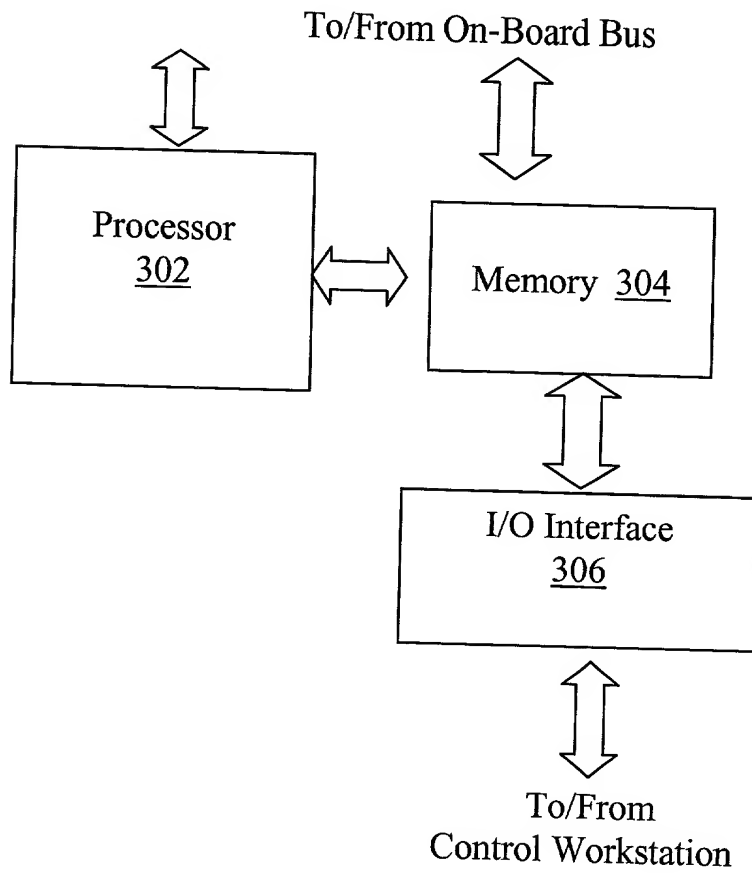
RLR = Reconfigurable Logic Resources  
 RIN = Reconfigurable Interconnect Network  
 MEM = Memory  
 DBR = Debugging Resources  
 CTX = Context (State) Elements  
 CR = Configuration Registers.

**Figure 2a**



**Figure 2b**

## On-Board Data Processing Resources - 102



**Figure 3**

Software - 400

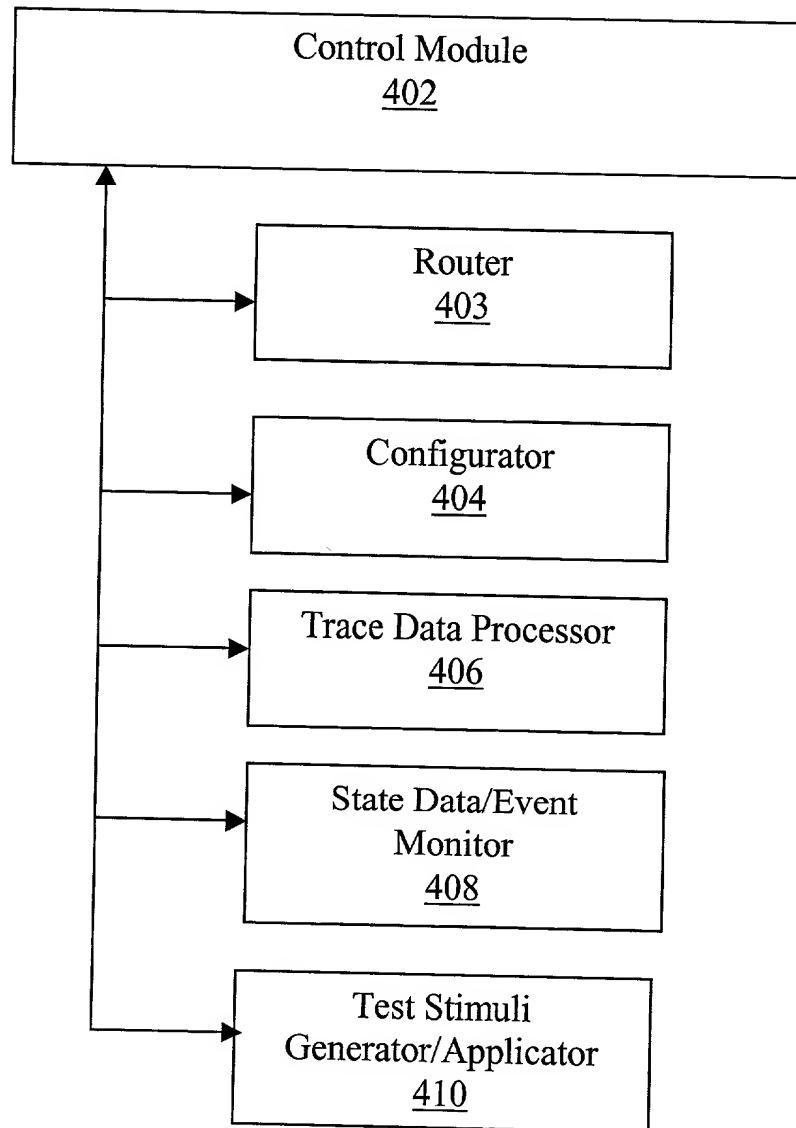


Figure 4

500

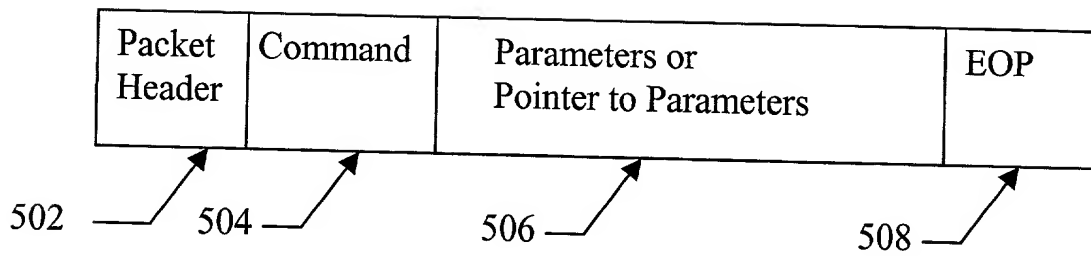


Figure 5

Emulator System - 600

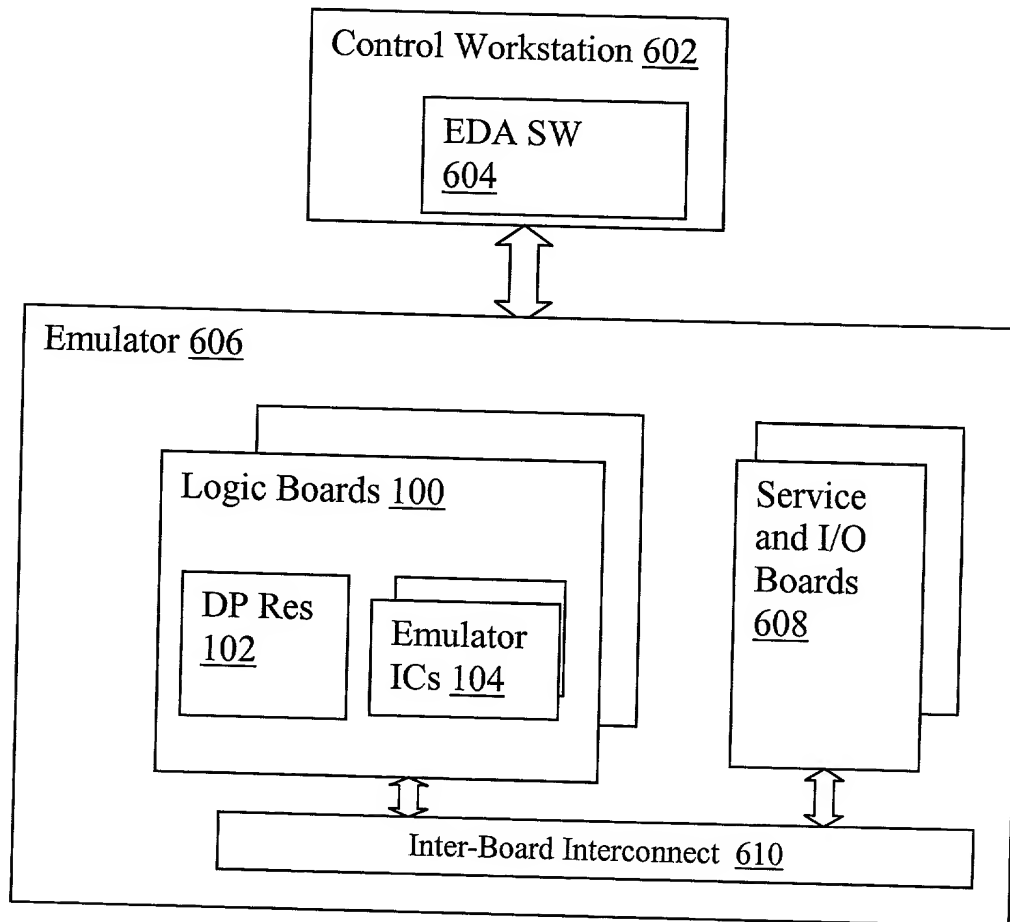
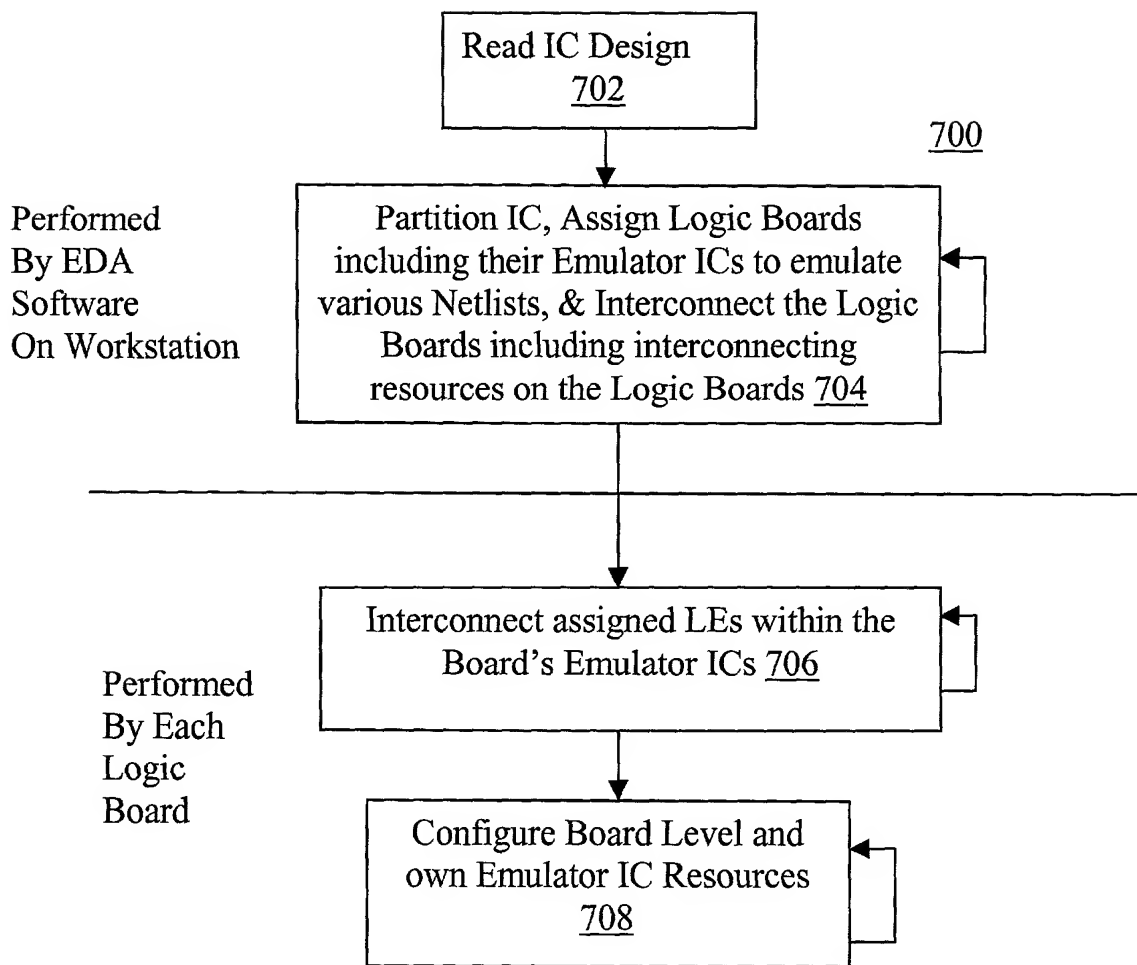
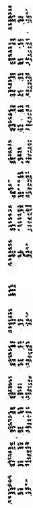


Figure 6



**Figure 7**

[illegible][illegible]



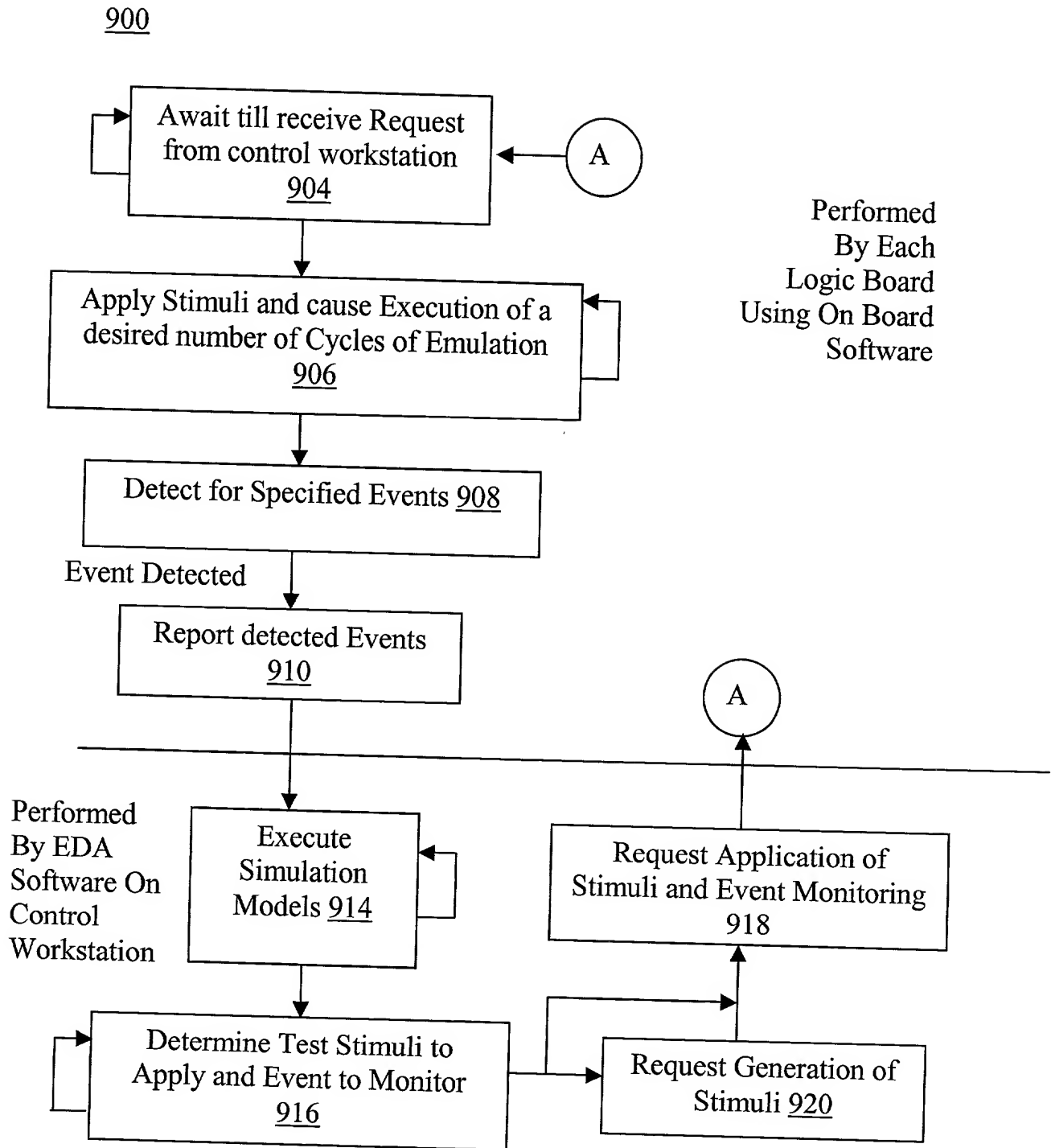
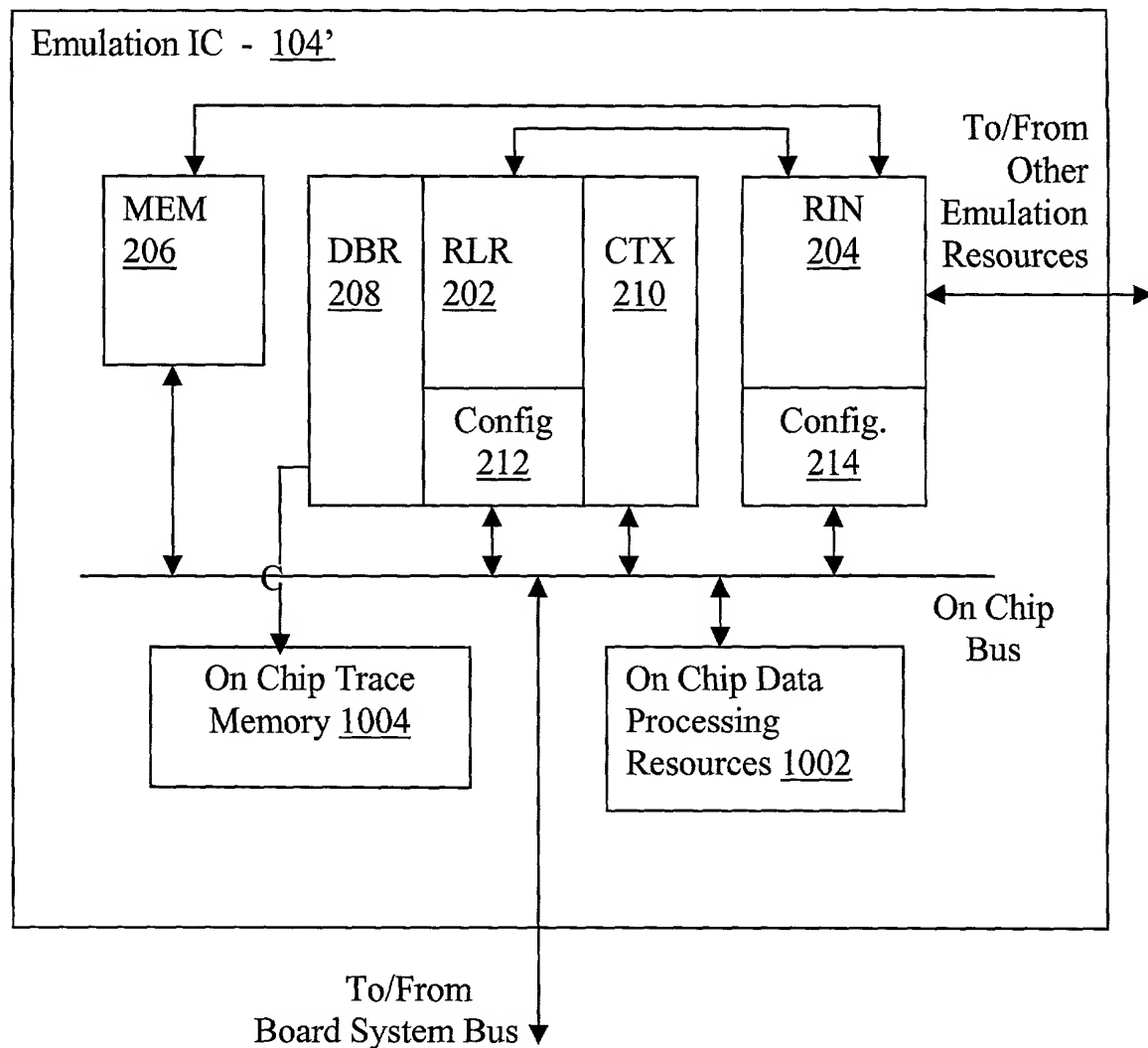


Figure 9



RLR = Reconfigurable Logic Resources  
 RIN = Reconfigurable Interconnect Network  
 MEM = Memory  
 DBR = Debugging Resources  
 CTX = Context (State) Elements  
 Config = Configuration registers.

**Figure 10**